

Attorney Docket No. 5347-205

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Gutnik et al.

U.S. Patent No.: 7,571,359

Application No.: 09/919,372

Issued: August 4, 2009

Filed: July 31, 2001

Confirmation No: 2525

Title: Clock Distribution Circuits and Methods of Operating Same that Use Multiple Clock Circuits Connected by Phase Detector Circuits to Generate and Synchronize Local Clock Signals

September 10, 2009

Commissioner for Patents

Attn: Certificate of Correction Branch

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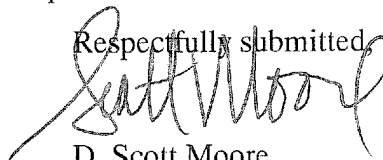
**REQUEST FOR ENTRY OF CERTIFICATE OF CORRECTION UNDER
No. 35 U.S.C. §254 AND 37 C.F.R. §1.322**

Sir:

The Assignee of record for the above-referenced patent hereby requests, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, that a Certificate of Correction be issued. This request is made in order to correct the mistakes incurred through the fault of the U.S. Patent and Trademark Office. The mistakes appearing in the patent are set forth with corrections on the Certificate of Correction attached herewith. The Assignee further directs the Commissioner's attention to the page 6, Claim 14 in the Amendment filed February 17, 2009

No fee is believed due. However, the Commissioner is authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted,

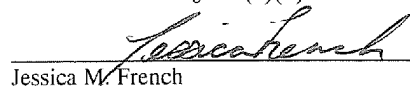


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CERTIFICATION OF ELECTRONIC TRANSMISSION

I hereby certify that this correspondence is being transmitted via the Office electronic filing system in accordance with § 1.6(a)(4) to the U.S. Patent and Trademark Office on September 10, 2009.



Jessica M. French

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

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PATENT NO. : 7,571,359
APPLICATION NO.: 09/919,372
ISSUE DATE: Aug. 4, 2009
INVENTOR(S) : Gutnik et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Claims:

Column 9, Claim 14, Line 7: Please correct "A method for distributing a clock signal," to read -- A method of generating clock signals, --.

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